



High Performance Computer Architecture (3rd Edition) (Addison-Wesley Series in Electrical & Computer Engineering)

Harold S. Stone

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This update of the popular book on computer architecture presents design ideas embodied in many high-performance machines and stresses techniques for evaluating them. Stone develops a proper understanding of the design process by treating the various trade-offs that exist in designing choices, and shows how good designs make efficient use of technology. Features *Teaches techniques for the design and analysis of high-performance machines *Develops students' intuition for design by treating various tradeoffs that exist in design choices *Discusses many important topics: RISC architectures, interconnection meshes, Cache coherent and multiprocessors, and Cache Memory. *Includes enhanced descriptions of RISC Processors *Expands material on Cache Memory Analysis *Current technology in RISC with a focused look on super scalar *Additional memory models and techniques for doing Cache design *New proposals for coherent memory systems in System C parallel processors *Both design and thought problems and problems with limiting parameters are provided 0201526883B04062001

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